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PATENT APPLICATION  
Docket No: 11948.25

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

TRANSMITTAL FOR SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop: DD  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Transmitted herewith for filing and pursuant to 37 C.F.R. § 1.97 is a Supplemental Information Disclosure Statement. Enclosed also are the following designated documents, as required under 37 C.F.R. §§ 1.97 and 1.98:

- ☒ Form PTO-1449 list of four references submitted for consideration.
- ☐ Legible copies of the listed references or their relevant portions.
- ☐ All English translations of each nonenglish reference, if any, within the possession, custody, control or availability of anyone designated in 37 C.F.R. § 1.56(c) (see 37 C.F.R. § 1.98(c)).

The following are included within the Supplemental Information Disclosure Statement if applicable and as required under 37 C.F.R. § 1.98:

- ☐ Concise explanation of relevance of each reference not in English and unaccompanied by an English translation.
- ☐ Statement that certain listed references not enclosed are substantially cumulative of an enclosed reference.

— Statement that certain listed references not enclosed were previously cited by or submitted to the Office in the identified prior application, filed on, which is relied upon for an earlier filing date under 35 U.S.C. ' 120.

In order to secure consideration of the items designated above, one or more of the following, if required, is also enclosed:

— Promptness Certification.

— Check No. \_\_\_\_\_ in the amount of \$ \_\_\_\_\_ constituting submission fee -- see 37 C.F.R. § 1.17(p)

— Petition for Consideration and Check No. \_\_\_\_\_ in the amount of \$ \_\_\_\_\_ -- see 37 C.F.R. § 1.17(i)(1).

X In the event that 37 C.F.R. § 1.97(c) applies and the Examiner is not satisfied that the Promptness Certification meets the requirements of 37 C.F.R. § 1.97(e), or in any other event remediable by a fee, please credit any over payment or charge any additional fees to Deposit Account No. 500843 of the undersigned.

DATED this 2 day of December 2004.

Respectfully submitted,



Kenneth E. Horton  
Attorney for Applicant  
Registration No. 39,481

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PATENT APPLICATION  
Docket No: 11948.25

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of	Rajeev Joshi et al.	)
		)
Serial No.:	10/731,453	) Art Unit
		) 2841
Confirmation No.:	4432	)
		)
Filed:	December 9, 2003	)
		)
For:	WAFER-LEVEL CHIP SCALE PACKAGE	)
	AND METHOD FOR FABRICATING	)
	AND USING THE SAME	)
		)
Examiner:	Unknown	)

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT  
UNDER 37 C.F.R. § 1.97

Mail Stop: DD  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Please find, pursuant to 37 C.F.R. § 1.98(a)(1), the enclosed Form PTO-1449 which contains a list of all patents, publications, or other items that have come to the attention of one or more of the individuals designated in 37 C.F.R. § 1.56(c). While no representation is made that any of these references may be “prior art” within the meaning of that term under 35 U.S.C. §§ 102 or 103, the enclosed list of references is disclosed so as to fully comply with the duty of disclosure set forth in 37 C.F.R. § 1.56.

Moreover, while no representation is made that a specific search of office files or patent office records has been conducted or that no better art exists, the undersigned attorney of record believes that the enclosed art is the closest to the claimed invention (taken in its entirety) of which the undersigned is presently aware, and no art which is closer to the claimed invention (taken in its entirety) has been knowingly withheld.

Pursuant to 37 C.F.R. § 1.491(b), since this application was filed after June 30, 2003, copies of all U.S. patents and U.S. patent application publications listed on the Form PTO-1449 are not being submitted.

Please credit any over payment or charge any additional fees to Deposit Account No. 500843 of the undersigned.

DATED this 2 day of December 2004.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Ken Horton", with a stylized flourish at the end.

Kenneth E. Horton  
Attorney for Applicant  
Registration No. 39,481

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KEH:jrj

Form PTO-1449

Applicant: Rajeev Joshi et al.

Serial No.: 10/731,453

Filing Date: December 9, 2003

For: WAFER-LEVEL CHIP SCALE PACKAGE AND METHOD FOR FABRICATING  
AND USING THE SAME

Sheet 1 of 1

Conf. No. 4432

Att'y Docket No. 11948.25

Group: 2841

### INFORMATION DISCLOSURE CITATIONS MADE BY APPLICANT

#### U.S. Patent Documents

<u>Examiner Initial*</u>	<u>Document Number</u>	<u>Issue Date</u>	<u>Name</u>	<u>Class</u>	<u>Sub Class</u>
_____ A1.	6,294,405	09/2001	Higgins, III	438	108
_____ A2.	6,350,668	02/2002	Chakravorty	438	612
_____ A3.	6,362,087	03/2002	Wang et al.	438	597
_____ A4.	6,417,089	07/2002	Kim et al.	438	612

#### **Prior Art Cited by Applicants**

While the filing of prior art statements is voluntary, the procedure is governed by the guidelines of Section 609 of the Manual of Patent Examining Procedure and 37 C.F.R. §§ 1.97 and 1.98. To be considered a proper prior art statement, Form PTO-1449 shall be accompanied by an explanation of relevance of each listed item, a copy of each listed patent or publication or other item of information and a translation of the pertinent portions of foreign documents (if an existing translation is readily available to the applicant), and should be submitted in a timely manner as set out in MPEP Sec. 609.

Examiners will consider all prior art citations submitted in conformance with 37 C.F.R. § 1.98 and MPEP Sec. 609 and place their initials adjacent the citations in the spaces provided on this form. Examiners will also initial citations not in conformance with the guidelines which may have been considered. A reference may be considered by the Examiner for any reason whether or not the citation is in full conformance with the guidelines. A line will be drawn through a citation if it is not in conformance with the guidelines AND has not been considered. A copy of the submitted form, as reviewed by the Examiner, will be returned to the applicant with the next communication. The original of the form will be entered into the application file.

Each citation initialed by the Examiner will be printed on the issued patent in the same manner as prior art cited by the Examiner on Form PTO-892.

The reference designations "A1", "A2", etc. (referring to Applicant's reference 1, Applicant's reference 2, etc.) will be used by the Examiner in the same manner as Examiner's reference designations "A", "B", "C", etc. on Office Action Form PTO-1142.

DOCS-#795481-v1-IDS\_1449.DOC

Examiner:

Date Considered:

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



PATENT APPLICATION  
Docket No: 11948.25

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In re application of	Rajeev Joshi et al.	)
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For:	WAFER-LEVEL CHIP SCALE PACKAGE	)
	AND METHOD FOR FABRICATING	)
	AND USING THE SAME	)
		)
Examiner:	Unknown	)

CERTIFICATE OF DEPOSIT UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence and the documents listed below are being deposited with the United States Postal Service as First Class Mail, postage prepaid, in an envelope addressed to: Mail Stop: DD, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 2, day of December 2004.

Kenneth E. Horton  
Attorney for Applicant  
Registration No. 39,481

Transmitted: Transmittal for Supplemental Information Disclosure Statement  
Supplemental Information Disclosure Statement  
Form PTO-1449 Listing of All References  
Postcard

KEH:jrr